

L Number	Hits	Search Text	DB	Time stamp
1	10	(shift\$3.in. or shift\$3.ab. or shift\$3.clm.) and (dual adj rail\$1) and Domino	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:01
2	1032	logic adj shift\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:01
3	5	(logic adj shift\$3) and (dual adj rail\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:02
4	1	708/209.ccls. and Domino	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:03
5	221	708/209.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:03
6	28	708/209.ccls. and dual	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:09
7	3469	barrel adj shift\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:09
8	15	(barrel adj shift\$3) and ((dual adj rail\$1) or Domino)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:12
9	10	faget.in. and roy.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/20 10:12
12	11	("4472788" "5231636" "5553010" "5671166" "5751614" "5773995" "5781457" "5802556" "5809320" "5819056" "5991786").PN.	USPAT	2004/10/20 10:23
13	1	6393446.URPN.	USPAT	2004/10/20 10:23
-	192	(708/209).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:50
-	5325	(377/69-).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 10:57
-	730156	shift\$1 or shifting or shifter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 10:58

-	1151	((377/69-).CCLS.) and (shift\$1 or shifting or shifter\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 10:59
-	75413	4.ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 10:59
-	283	4.ti. and ((377/69-).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 10:59
-	311710	4.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 10:59
-	681	4.ab. and ((377/69-).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 11:47
-	161	4.ab. and ((708/209).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 11:00
-	127	4.ti. and ((708/209).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 11:00
-	1576	"logic gates" with (shift\$1 or shifting or shifter\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 11:48
-	57	("logic gates" with (shift\$1 or shifting or shifter\$1)) and ((377/69-).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 13:02
-	684	((377/69-).CCLS.) and (shift\$1 or shifting or shifter\$1)) and logic\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 13:13
-	2	("5793654").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 13:35
-	2	("5513362").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 13:36
-	2	("5798952").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 13:36
-	2	("4782457").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 13:36

-	2	("4779220").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 15:52
-	1	("6457474").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/03 15:52
-	20	"5553010"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 17:34
-	15	5553010.URPN.	USPAT	2002/10/09 17:45
-	9	("3996566" "4411009" "4472788" "4488252" "4785393" "4831571" "4839839" "5130941" "5155698").PN.	USPAT	2002/10/09 17:45
-	130	(conversion or converting or convert) adj tree	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/10/09 18:14
-	21593	708/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 08:30
-	86928	(rotat\$3 or shift\$3) and multiplex\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 08:32
-	3111	708/\$.ccls. and ((rotat\$3 or shift\$3) and multiplex\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 08:31
-	3121	(rotat\$3 or shift\$3).ti. and multiplex\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 08:32
-	151	((rotat\$3 or shift\$3).ti. and multiplex\$3) and 708/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 08:33
-	150	((rotat\$3 or shift\$3).ti. and multiplex\$3) and 708/\$.ccls.) and (control\$3 or select\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 08:33
-	47	((rotat\$3 or shift\$3).ti. and multiplex\$3) and 708/\$.ccls.) and (control\$3 or select\$3)) and transistor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 09:07
-	24	rotat\$.ti. and multiplex\$3 and 708/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 09:08
-	198	708/209.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 12:31

-	1609	377/64-81.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 12:31
-	461	377/64-81.ccls. and (shift\$3 or rotat\$3).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 12:31
-	59	(377/64-81.ccls. and (shift\$3 or rotat\$3).ti.) and multiplex\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/24 12:32
-	263	(dual adj rail\$1) and shift\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:51
-	34805	roy.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:51
-	9	roy.in. and faget.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:55
-	145	((dual adj rail\$1) and shift\$3) and transistor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:55
-	135	((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:56
-	13	((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and 708/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:58
-	57	((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and shar\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:58
-	51	((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and shar\$3) not (((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and 708/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 16:59
-	8	((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and shar\$3) not (((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and 708/\$.ccls.) and (shar\$3.ti. shar\$3.ab. shar\$3.clm.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/17 17:00
-	43	((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and shar\$3) not (((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and 708/\$.ccls.) not (((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and shar\$3) not (((dual adj rail\$1) and shift\$3) and transistor\$1) and control\$3) and 708/\$.ccls.) and (shar\$3.ti. shar\$3.ab. shar\$3.clm.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:43

-	531023	shift\$3.ti. or shift\$3.ab. or shift\$3.clm.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:44
-	153	(shift\$3.ti. or shift\$3.ab. or shift\$3.clm.) and (Domino or (dual adj rail\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:44
-	53	((shift\$3.ti. or shift\$3.ab. or shift\$3.clm.) and (Domino or (dual adj rail\$1))) and shar\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:45
-	7	((shift\$3.ti. or shift\$3.ab. or shift\$3.clm.) and (Domino or (dual adj rail\$1))) and shar\$3) and 708/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:50
-	5377	377/69-130.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:51
-	1059	377/69-130.ccls. and (shift\$3.ti. or shift\$3.ab. or shift\$3.clm.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:51
-	417	(377/69-130.ccls. and (shift\$3.ti. or shift\$3.ab. or shift\$3.clm.)) and transistor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:51
-	362	((377/69-130.ccls. and (shift\$3.ti. or shift\$3.ab. or shift\$3.clm.)) and transistor\$1) and control\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:52
-	4	((377/69-130.ccls. and (shift\$3.ti. or shift\$3.ab. or shift\$3.clm.)) and transistor\$1) and control\$5) and ((shift\$3.ti. or shift\$3.ab. or shift\$3.clm.) and (Domino or (dual adj rail\$1)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:52
-	311	377/64.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:56
-	287	(shift\$3.ti. or shift\$3.ab. or shift\$3.clm.) and 377/64.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 12:56
-	24	((shift\$3.ti. or shift\$3.ab. or shift\$3.clm.) and 377/64.ccls.) and shar\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/18 13:42



Welcome
United States Patent and Trademark Office

IEEE Xplore®
1 Million Documents
1 Million Users

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

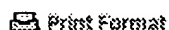
- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet



Print Format

Your search matched **72** of **1082760** documents.

A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set
Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 Two nondeterministic event building methods derived from the Barrel Shifter

Harangozo, G.;

Simulation Symposium, 1997. Proceedings. 30th Annual , 7-9 April 1997

Pages:137 - 144

[\[Abstract\]](#) [\[PDF Full-Text \(560 KB\)\]](#) IEEE CNF

2 Statistically optimized asynchronous barrel shifters for variable length codecs

Beerel, P.A.; Sangyun Kim; Pei-Chuan Yeh; Kyeounsoo Kim;

Low Power Electronics and Design, 1999. Proceedings. 1999 International Symposium on , 16-17 Aug. 1999

Pages:261 - 263

[\[Abstract\]](#) [\[PDF Full-Text \(188 KB\)\]](#) IEEE CNF

3 Parallel digital signal filtering on barrel shifter computers

Angelopoulos, G.; Pitas, I.;

Circuits and Systems, 1995. ISCAS '95., 1995 IEEE International Symposium on , Volume: 2 , 28 April-3 May 1995

Pages:1368 - 1371 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) IEEE CNF

4 A VME barrel shifter system for event reconstruction for up to 3 Gbps signal trains

Sasaki, O.; Nomachi, M.; Ohsaka, T.K.; Fujii, H.;

Nuclear Science Symposium and Medical Imaging Conference, 1992., Conference Record of the 1992 IEEE , 25-31 Oct. 1992

Pages:511 - 513 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) IEEE CNF

5 Fast parallel DSP algorithms on barrel shifter computers

Angelopoulos, G.; Pitas, I.;

Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] , Volume: 44 , Issue: 8 , Aug. 1996

Pages:2126 - 2129

[\[Abstract\]](#) [\[PDF Full-Text \(340 KB\)\]](#) **IEEE JNL**

6 Multilevel barrel shifter for CORDIC design

Yih, S.-J.; Cheng, M.; Feng, W.-S.;

Electronics Letters , Volume: 32 , Issue: 13 , 20 June 1996

Pages:1178 - 1179

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) **IEE JNL**

7 Barrel shifter-a close approximation to the completely connected network in supporting dynamic tree structured computations

Keqin Li;

Aerospace and Electronics Conference, 1997. NAECON 1997., Proceedings of the IEEE 1997 National , Volume: 1 , 14-17 July 1997

Pages:202 - 215 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(1208 KB\)\]](#) **IEEE CNF**

8 Power comparisons for barrel shifters

Acken, K.P.; Irwin, M.J.; Owens, R.M.;

Low Power Electronics and Design, 1996., International Symposium on , 12-14 Aug. 1996

Pages:209 - 212

[\[Abstract\]](#) [\[PDF Full-Text \(336 KB\)\]](#) **IEEE CNF**

9 Fully pipelined TSPC barrel shifter for high-speed applications

Pereira, R.; Michell, J.A.; Solana, J.M.;

Solid-State Circuits, IEEE Journal of , Volume: 30 , Issue: 6 , June 1995

Pages:686 - 690

[\[Abstract\]](#) [\[PDF Full-Text \(480 KB\)\]](#) **IEEE JNL**

10 A VME barrel shifter system for event reconstruction for up to 3 Gbps signal trains

Sasaki, O.; Nomachi, M.; Ohsaka, T.K.; Fujii, H.;

Nuclear Science, IEEE Transactions on , Volume: 40 , Issue: 4 , Aug 1993

Pages:603 - 606

[\[Abstract\]](#) [\[PDF Full-Text \(368 KB\)\]](#) **IEEE JNL**

11 A new logical topology based on barrel shifter network over an all optical network

Chaki, N.; Chaki, R.; Saha, B.; Chattopadhyay, T.;

Local Computer Networks, 2003. LCN '03. Proceedings. 28th Annual IEEE International Conference on , 20-24 Oct. 2003

Pages:283 - 284

[\[Abstract\]](#) [\[PDF Full-Text \(218 KB\)\]](#) **IEEE CNF**

12 The BELLE event building system

Suzuki, S.Y.; Matsuo, H.; Fujii, H.; Sasaki, O.; Igarashi, Y.; Nagasaka, Y.; Watase, Y.; Sakamoto, H.; Tairadate, M.;
Real Time Conference, 1999. Santa Fe 1999. 11th IEEE NPSS , 14-18 June 1999
Pages:24 - 27

[\[Abstract\]](#) [\[PDF Full-Text \(288 KB\)\]](#) **IEEE CNF**

13 The Belle event building system

Suzuki, S.Y.; Matsuo, H.; Fujii, H.; Sasaki, O.; Igarashi, Y.; Nagasaka, Y.; Watase, Y.; Sakamoto, H.; Tairadate, M.;
Nuclear Science, IEEE Transactions on , Volume: 47 , Issue: 2 , April 2000
Pages:61 - 64

[\[Abstract\]](#) [\[PDF Full-Text \(204 KB\)\]](#) **IEEE JNL**

14 Low power parallel Huffman decoding

Chia-Hsing Lin; Chein-Wei Jen;
Electronics Letters , Volume: 34 , Issue: 3 , 5 Feb. 1998
Pages:240 - 241

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) **IEE JNL**

15 Pipelined TSPC barrel shifter with scan test facilities for VLSI implementation of high speed DSP applications

Pereira, R.; Michell, J.A.; Solana, J.M.;
Euro ASIC '92, Proceedings. , 1-5 June 1992
Pages:405 - 406

[\[Abstract\]](#) [\[PDF Full-Text \(208 KB\)\]](#) **IEEE CNF**

[1](#) [2](#) [3](#) [4](#) [5](#) [Next](#)
